10205.023 **J. J.**PATENT **J. J. J.**

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

: Art Unit:

Rubacha et al.

Examiner:

Serial No.:

Date: January 12, 2000

Filed: concurrently

For: SOFT MUTE CIRCUIT

I HEREBY CERTIFY THAT THIS CORRESPONDENCE IS BEING DEPOSITED WITH THE UNITED STATES POSTAL SERVICE AS EXPRESS MAIL IN AN ENVELOPE ADDRESSED TO: COMMISSIONER OF PATENTS AND TRADEMARKS;

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JAN. 13, 2000

Date of Deposit

Paul F. Wille

1.13.00

Signature

Date

DISCLOSURE STATEMENT UNDER RULE 56

Honorable Commissioner of Patents and Trademarks, Washington, D.C. 20231

SIR:

It is respectfully requested that the art listed below be considered in the examination of the subject application and made of record therein. A copy of the art listed below is enclosed.

4,983,927.....

January 8, 1991

discloses amplifier OP having gain

Torazzina adjusted by voltage on node P,

which proceeds through "mute" and

"stant-by" levels on way to "cut-

off"

10205.023 PATENT

It is respectfully submitted that the invention as described and claimed is not disclosed or suggested by the prior art listed above.

Respectfully submitted,

Paul F. Wille

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